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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10082160	FILING DATE 02/26/2002	CLASS 438	SUBCLASS 14	GAU 2812	EXAMINER THAO LE
**APPLICANTS: Takahashi Youji; Iida Tsutomu; Umemoto Tsuyoshi; Kashibe Makoto;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials				500.41283X00	
TITLE : Power supply, a semiconductor making apparatus and a semiconductor wafer fabricating method using the same					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Primary Examiner	
Amount Due	Date Paid	PREPARED FOR ISSUE	
<input type="checkbox"/> TERMINAL DISCLAIMER		Applicati n Examiner	
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